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AF-2827



PATENT APPLICATION

**RESPONSE UNDER 37 CFR §1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER ART UNIT 2827**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Nobuaki HASHIMOTO

Group Art Unit: 2827

Application No.: 09/486,561

Examiner: L. Cruz

Filed: February 29, 2000

Docket No.: 105030

For: SEMICONDUCTOR DEVICE AND METHOD THEREOF, CIRCUIT BOARD AND
ELECTRONIC INSTRUMENT

RESUBMISSION OF AMENDMENT AFTER FINAL REJECTION

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

As evidenced by the attached date-stamped postcard receipt, applicant filed a Petition for Extension of Time and Amendment After Final Rejection on December 6, 2002.

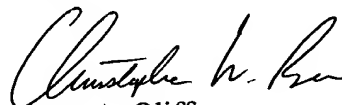
However, applicant has not yet received any further action from the Patent Office. According to the information on PAIR, the December 6, 2002 response has not yet been posted. The undersigned has attempted to contact the Examiner to confirm whether or not the December 6, 2002 response has been received, but without success.

Accordingly, applicant is re-submitting the December 6, 2002 response for immediate entry and consideration.

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Should the Examiner have any questions, the Examiner is invited to contact applicant's undersigned representative at the telephone number listed below.

Respectfully submitted,



James A. Oliff

Registration No. 27,075

Christopher W. Brown

Registration No. 38,025

JAO:CWB/rxg

Attachments:

Date-stamped Postcard Receipt
Petition for Extension of Time
Amendment After Final Rejection with Appendix

Date: January 14, 2003

OLIFF & BERRIDGE, PLC
P.O. Box 19928
Alexandria, Virginia 22320
Telephone: (703) 836-6400

<p>DEPOSIT ACCOUNT USE AUTHORIZATION Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461</p>
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AMENDMENT AFTER FINAL REJECTION UNDER 37 CFR §1.116

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

In reply to the August 14, 2002 Office Action, please amend the above-identified
application as follows:

IN THE CLAIMS:

Please cancel claims 4 and 10 without prejudice to or disclaimer of the subject matter
contained therein.

Please replace claims 1, 8, 11, 12 and 14-16 as follows:

1. (Three Times Amended) A method of manufacturing a semiconductor device
comprising:

a first step of interposing an adhesive between a surface of a substrate on
which an interconnect pattern is formed and a surface of a semiconductor chip on which
electrodes are formed, said adhesive having conductive particles dispersed therein; and

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